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PAGE 1/13 * RCVD AT 6/24/2005 10:31:40 AM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/1 * DNIS:8729306 * CSID:Haynes and Boone, LLP * DURATION (mm:ss):03:26

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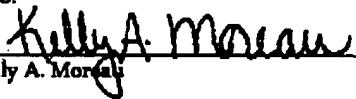
REPLY UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	§	Attorney Docket No.:
Chi-Chun Chen, et al.	§	2002-0066 / 24061.461
Serial No.: 10/600,393	§	Customer No. 42717
Filed: June 20, 2003	§	Group Art Unit: 2822
For: METHOD OF FORMING DUAL GATE INSULATOR LAYERS FOR CMOS APPLICATIONS	§	Examiner: Toniae M. Thomas
	§	Confirmation No.: 8529

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Kelly A. Moran

Sir:

AMENDMENT UNDER 37 C.F.R. §1.116
IN REPLY TO SECOND ADVISORY ACTION

This Amendment is submitted in reply to the Second Advisory Action mailed on June 17, 2005, and in reply to the Office Action mailed on March 25, 2005. (Please see the pages that follow).